

FIG.1 PRIOR ART

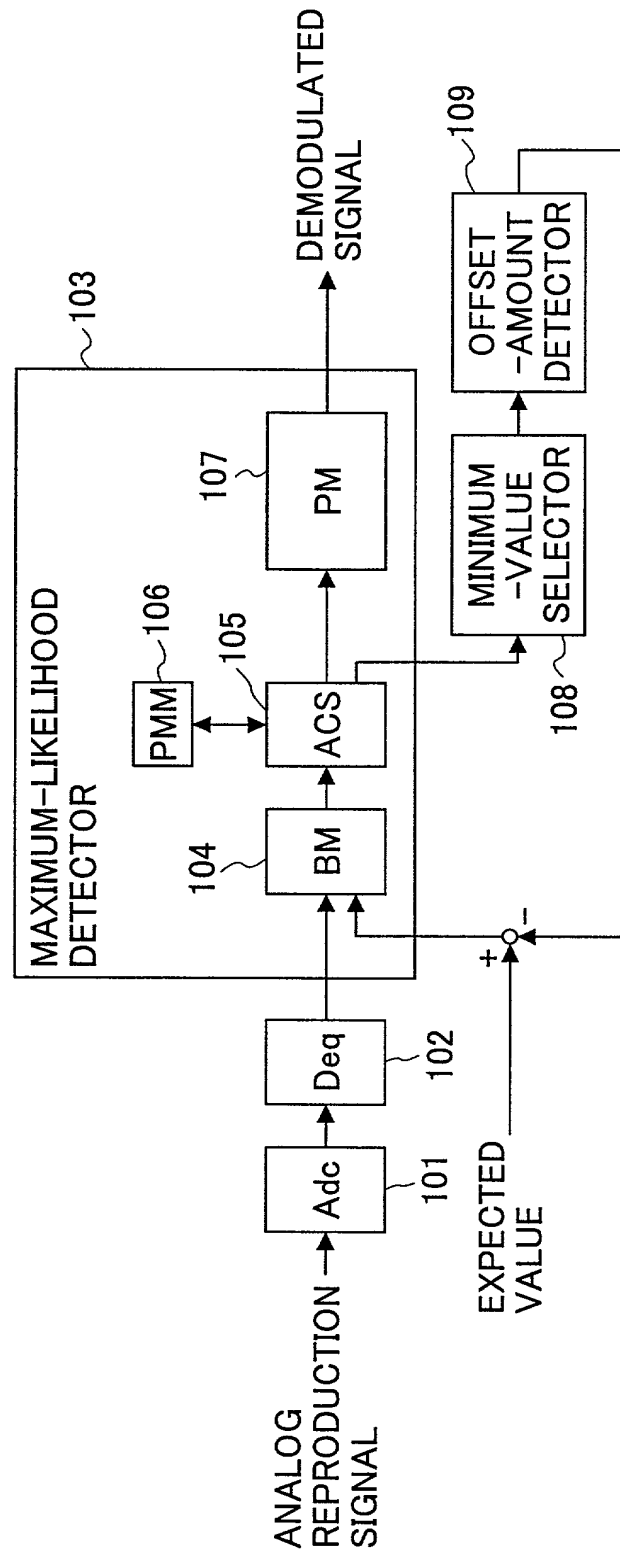


FIG.2 PRIOR ART

100

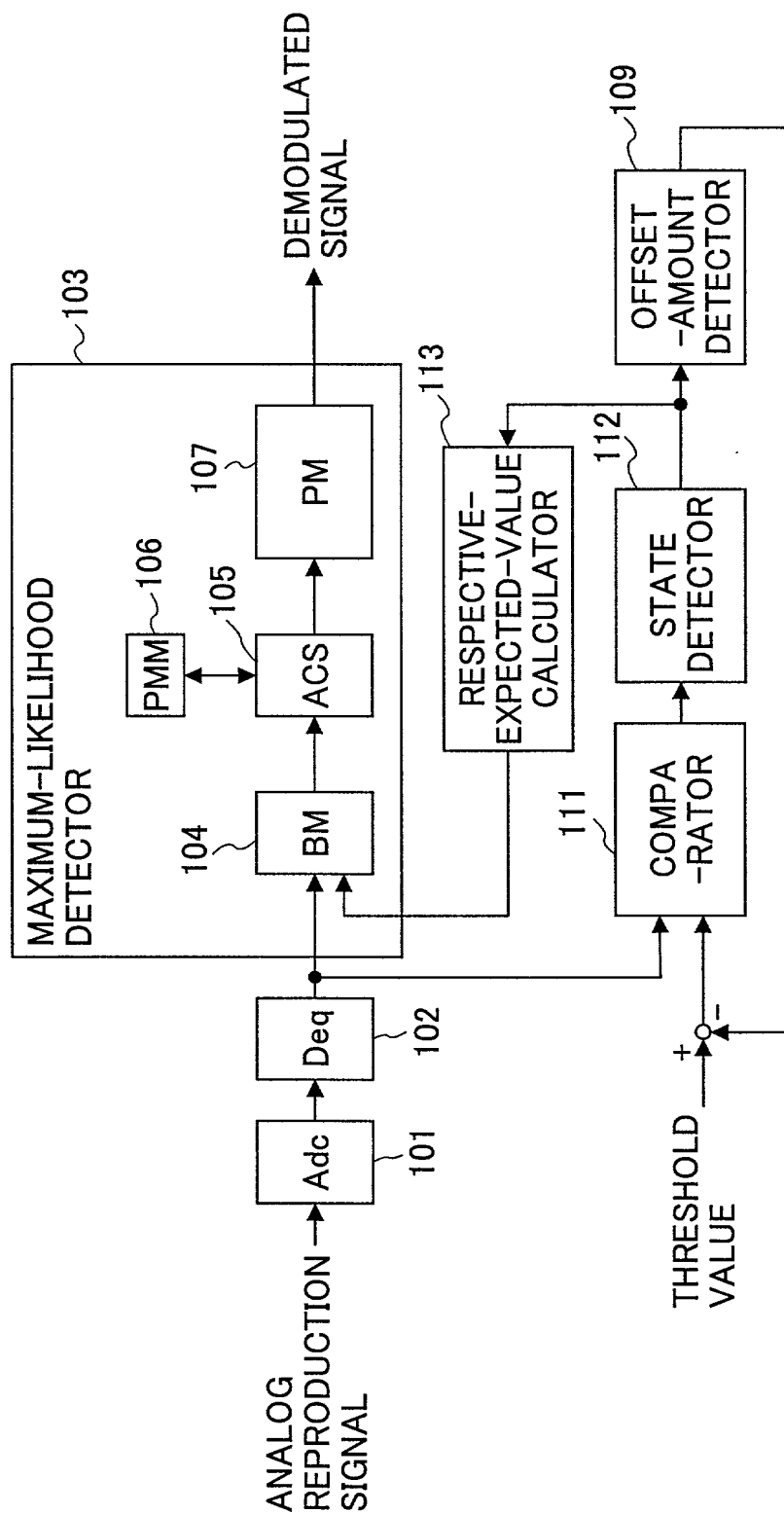


FIG. 3 PRIOR ART

100

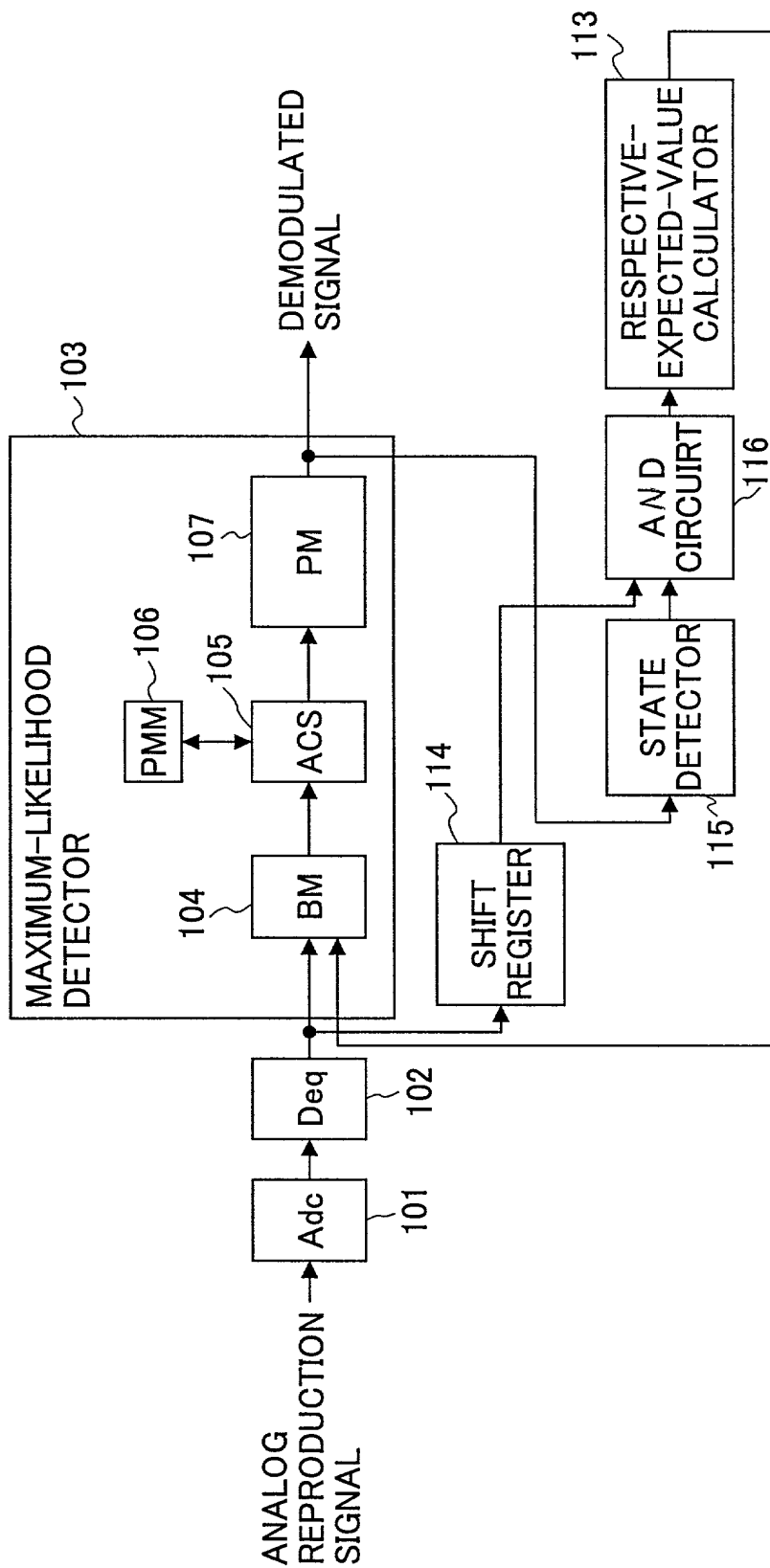


FIG.4

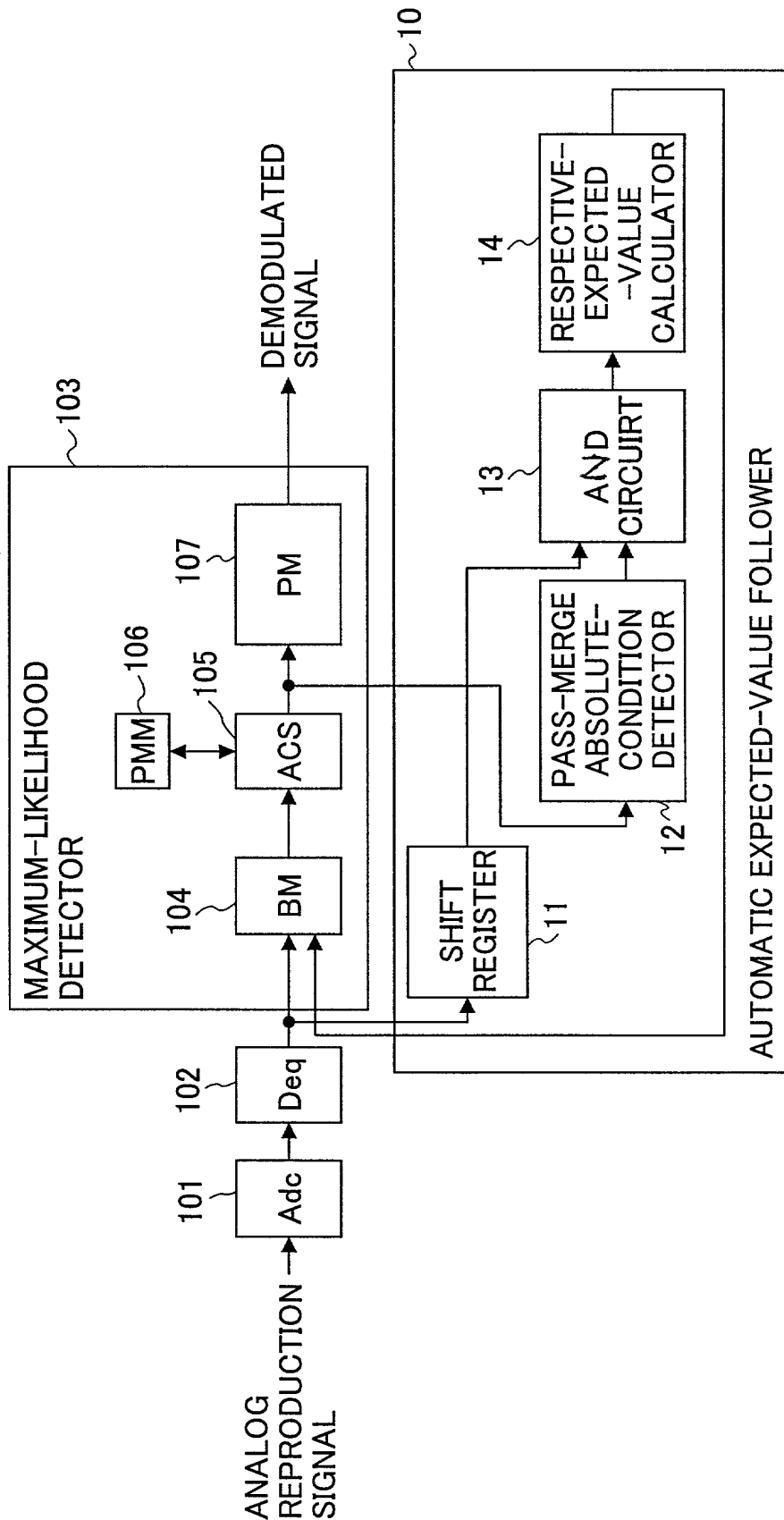


FIG.5

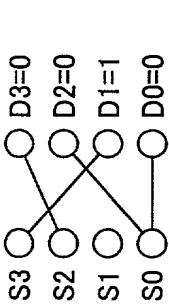
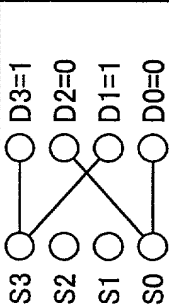
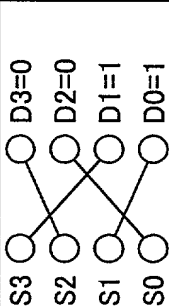
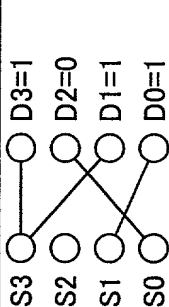
COMBINATION OF CONDITIONS	(1)	(2)	(3)	(4)
	(EXPRESSION 11) PASS (EXPRESSION 13) MEMORY	(EXPRESSION 12) PASS (EXPRESSION 13) MEMORY	(EXPRESSION 11) PASS (EXPRESSION 14) MEMORY	(EXPRESSION 12) PASS (EXPRESSION 14) MEMORY
TRELLIS DIAGRAM				

FIG. 6

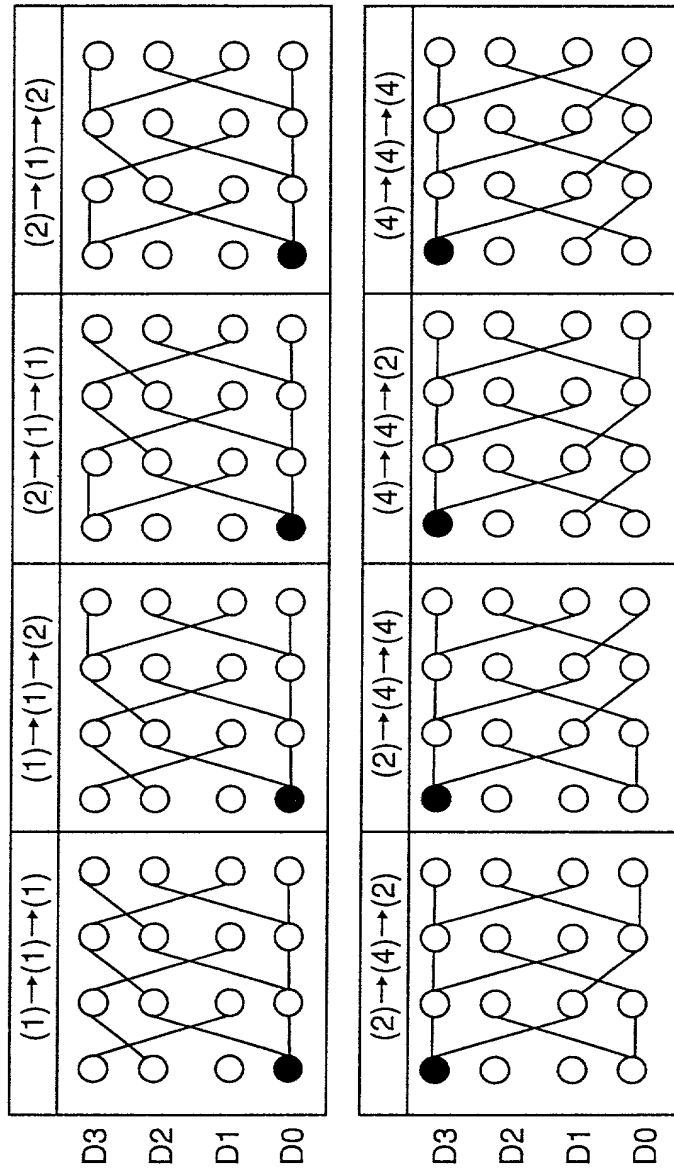


FIG. 7

107

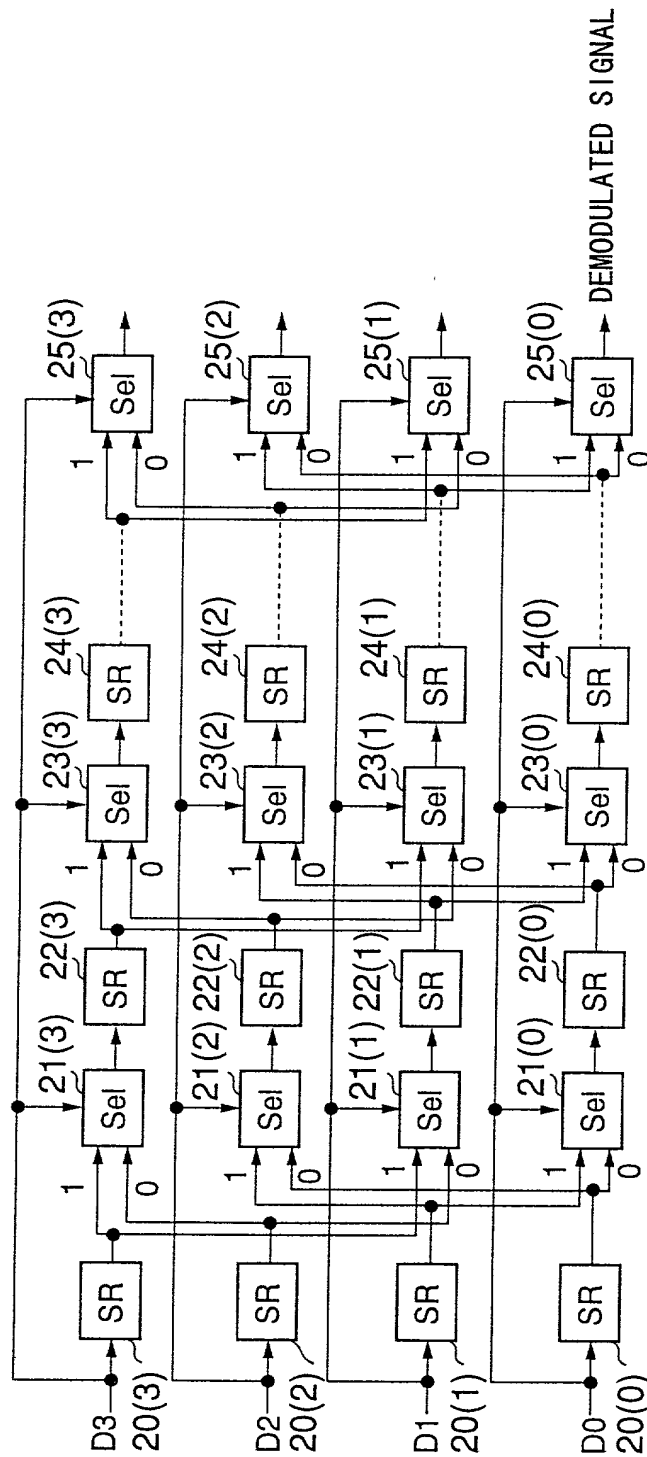


FIG. 8

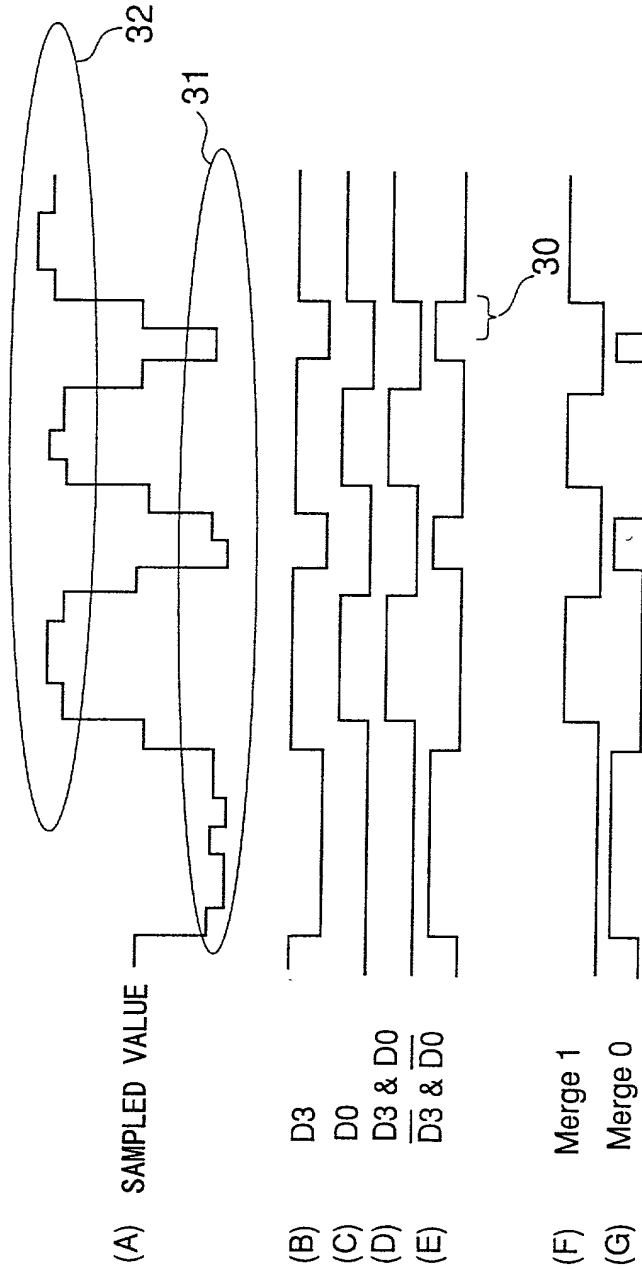




FIG. 9

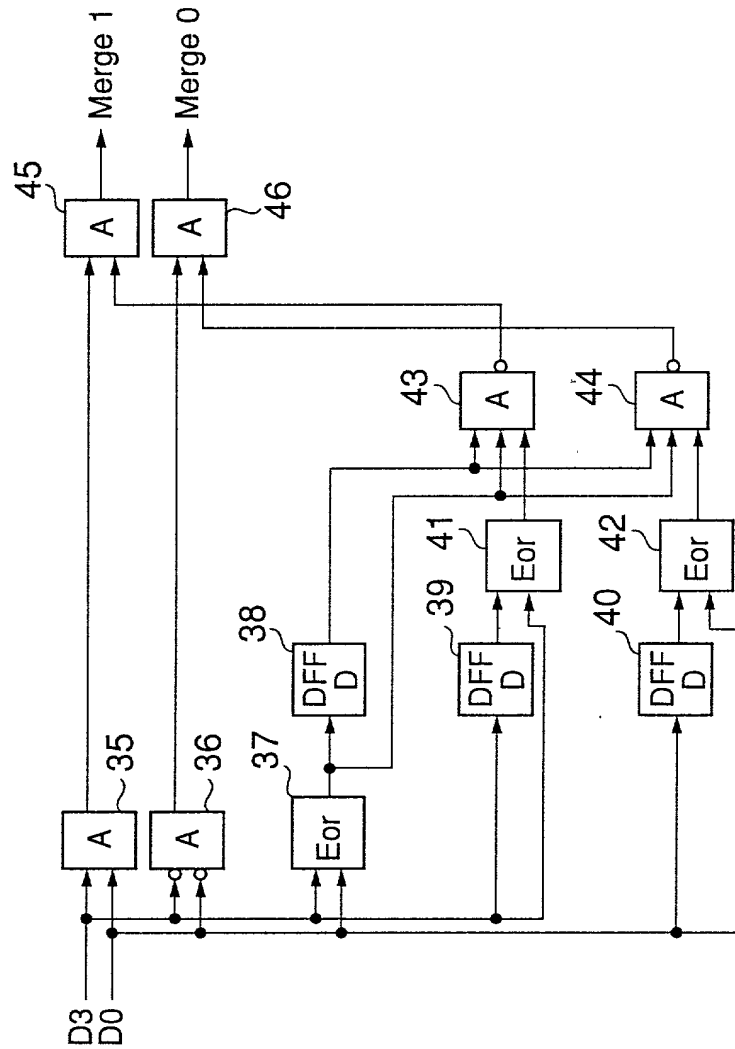


FIG.10

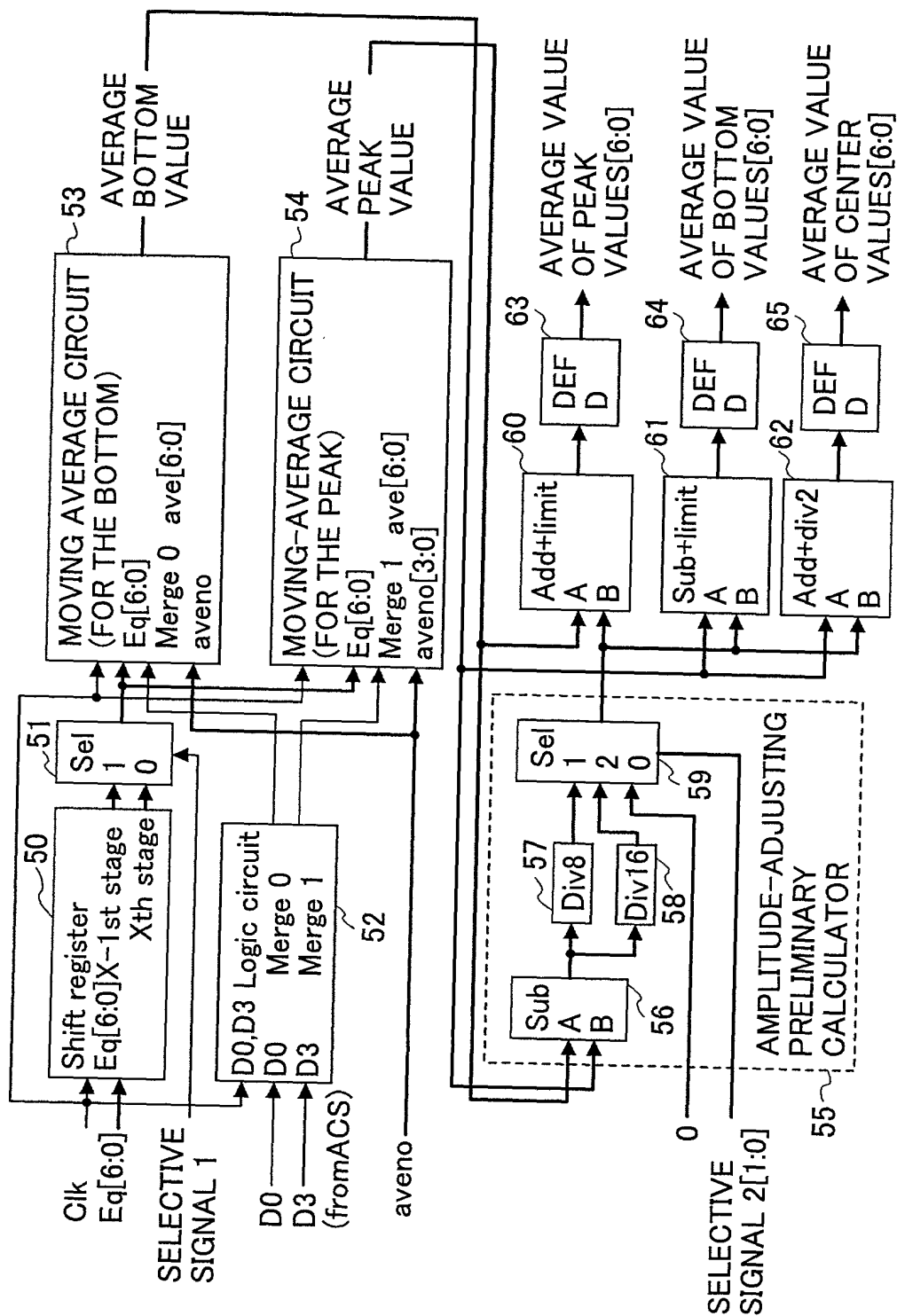
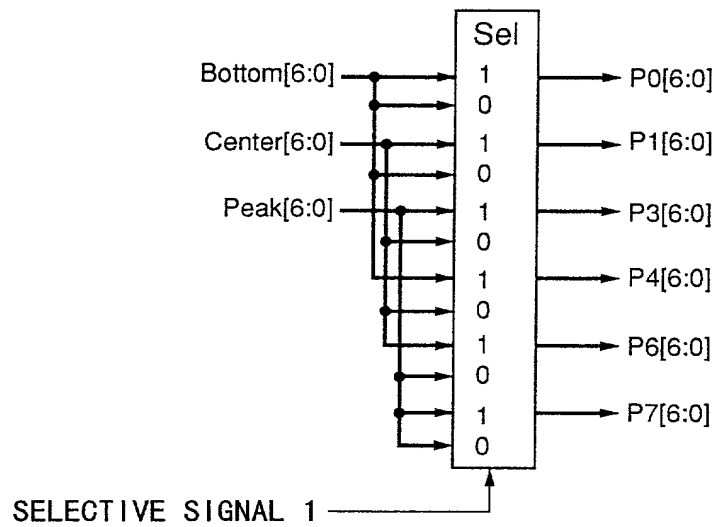


FIG. 11



0941.65686-0011

FIG. 12

2

